

ABSTRACT

1 A bimodal power data link transceiver device (33) is
2 provided. The device comprises a transceiver integrated
3 circuit (IC) (14); wherein the IC comprises an oscillator
4 (150), a frequency reference port, and a RF output port.
5 A VCO (12) is coupled to the oscillator and a direct
6 digital synthesizer (15) is coupled to the frequency
7 reference port. The combination allows the IC to operate
8 below 200 MHz. In addition, an external power amplifier
9 (19) is connected to the RF output port thus allowing for
10 burst RF communications at a higher power than the
11 quiescent receive mode.

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